



Wojciech Paszke

Dr Paszke was born in Zielona Góra, Poland, in 1975. He received his M.Sc. and Ph.D. degrees, both in electrical engineering, from the Technical University of Zielona Góra, in 2000 and 2005, respectively. Since 2005, he has been an Assistant Professor on the Faculty of Electrical Engineering, Computer Science and Telecommunications of the University of Zielona Góra. His general research interests include multidimensional (n-D) systems, repetitive processes and delayed systems with particular attention to computational aspects.